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Dated: 3/18/05

Signature:

Vickie Hoeft
(Vickie Hoeft)

Docket No.: 594728804US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kim et al.

Application No.: 09/989,580

Confirmation No.: 9737

Filed: November 20, 2001

Art Unit: 2661

For: BIDIRECTIONAL BRIDGE CIRCUIT
HAVING HIGH COMMON MODE
REJECTION AND HIGH INPUT
SENSITIVITY

Examiner: Not Yet Assigned

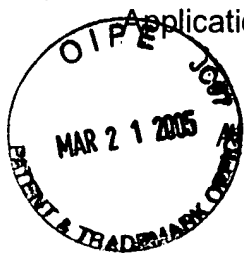
INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).



Application No.: 09/989,580

Docket No.: 594728804US

A copy of only those references listed below is attached:

Other Documents
Svelto, F. et al., "A 1.3 GHz Low-Phase Noise Fully Tunable CMOS LC VCO", IEEE Journal On Solid State Circuits, March 2000, pp. 356-361, Vol. 35, No. 3.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0665, under Order No. 594728804US.

Dated: March 18, 2005

Respectfully submitted,

By Maurice J. Pirio

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PERKINS COIE LLP

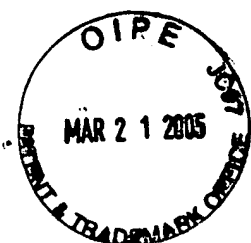
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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	09/989,580-Conf. #9737
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Filing Date	November 20, 2001
				First Named Inventor	Gyudong Kim
				Art Unit	2661
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	594728804US
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-09/989,487		Gyudong	
		US-09/989,647		Gyudong	
		US-10/371,220		Gyudong et al.	
		US-5,675,584	10-07-1997	DEOG-KYOON JEONG	
		US-6,011,444	01-04-2000	Takada	
		US-6,693,985-A1	02-17-2004	Hung Li	
		US-6,717,478	04-06-2004	Ook Kim	
		US-6,809,567	10-26-2004	Ook Kim	
		US-6,845,461	01-18-2005	Ook Kim	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Svelto, F. et al., "A 1.3 GHz Low-Phase Noise Fully Tunable CMOS LC VCO", IEEE Journal On Solid State Circuits, March 2000, pp. 356-361, Vol. 35, No. 3, 2000 IEEE.	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
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